



UNIVERSAL CD ADAPTOR

# CD-FM1

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# 1. ADJUSTMENT

## 1.1 DC OFFSET ADJUSTMENT

### ● Connection Diagram

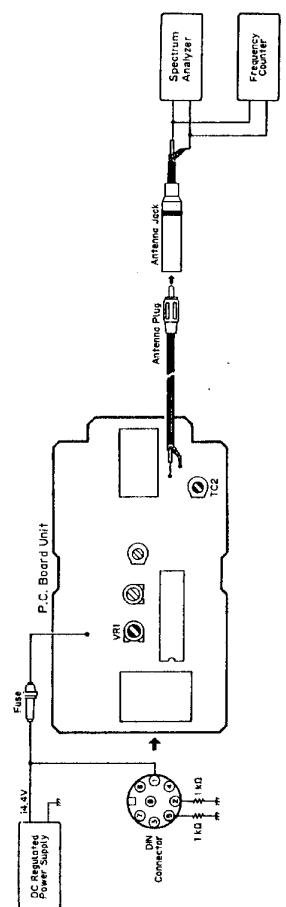


Fig. 1

### ● To Adjust

1. Adjust VR1 so that the leakage of 38 kHz signal becomes minimum.

## 1.2 FREQUENCY ADJUSTMENT

### ● Connection Diagram (shown in Fig. 1)

- To Adjust
1. Adjust TC2 so that the frequency counter reads 88.1 MHz.

## 1.3 SEPARATION ADJUSTMENT

### ● Connection Diagram

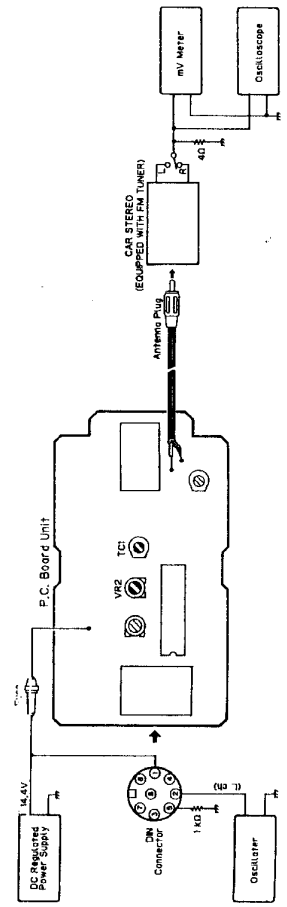


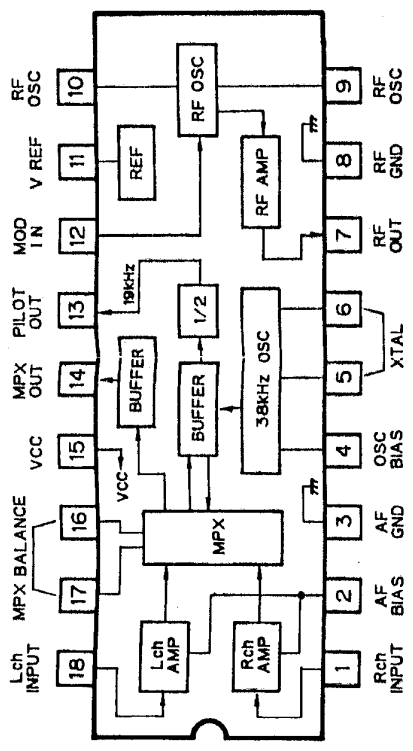
Fig. 2

### ● To Adjust

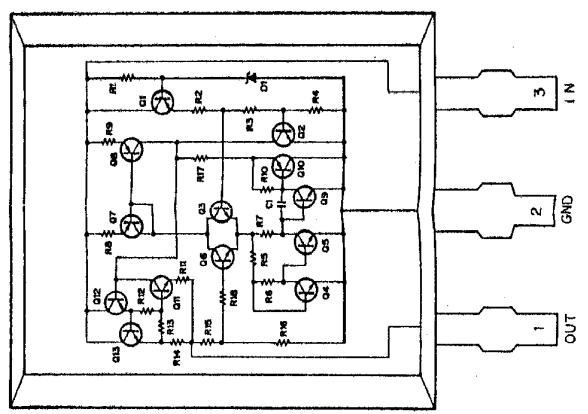
1. Input the 1 kHz, -10 dB (μV) signal into the L-channel.
2. Adjust VR2 and TC1 so that the signal leakage to the R-channel (crosstalk) becomes minimum.

### ● IC

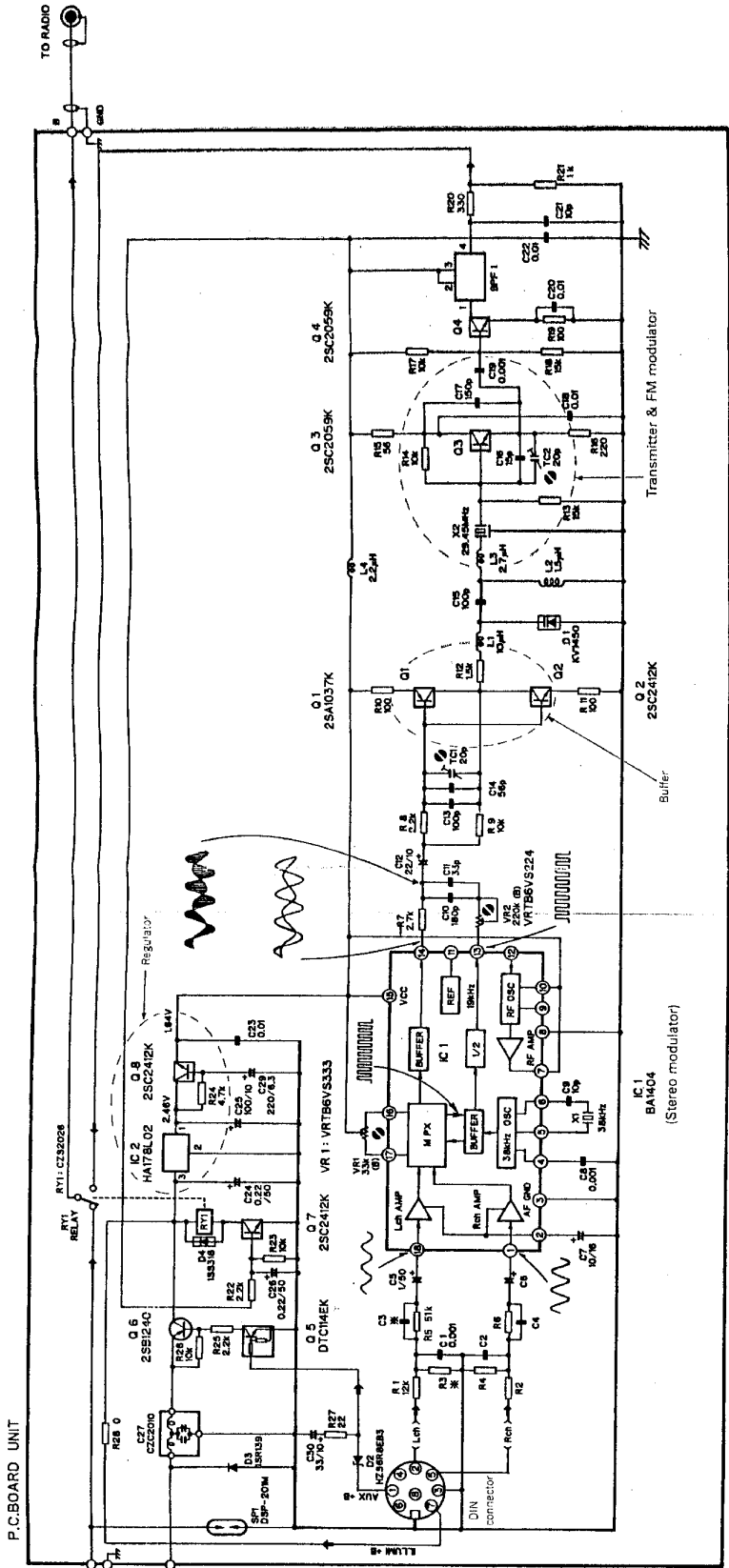
IC1: BA1404



IC2: HA178L02



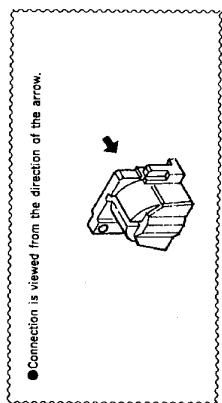
2. SCHEMATIC CIRCUIT DIAGRAM



● P.C. Board unit

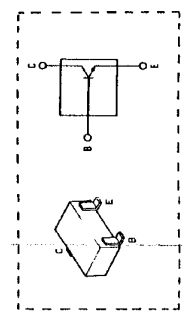
	CD-FM1/UC	CD-FM1/EW
R3	5.6 k	3.3 k
R4	5.6 k	3.3 k

	CD-FM1/UC	CD-FM1/EW
C3	0.0015	0.001
C4	0.0015	0.001

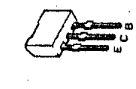


● Transistor

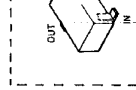
- 25C2059K
- 25C2412K



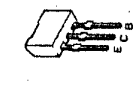
25A1037K



DTC114EK



25B1240



NOTE:  
 indicates a chip resistor.  
 indicates a chip capacitor.  
 indicates a chip transistor.  
 indicates a chip diode.

Fig. 3

3. CONNECTION DIAGRAM

A

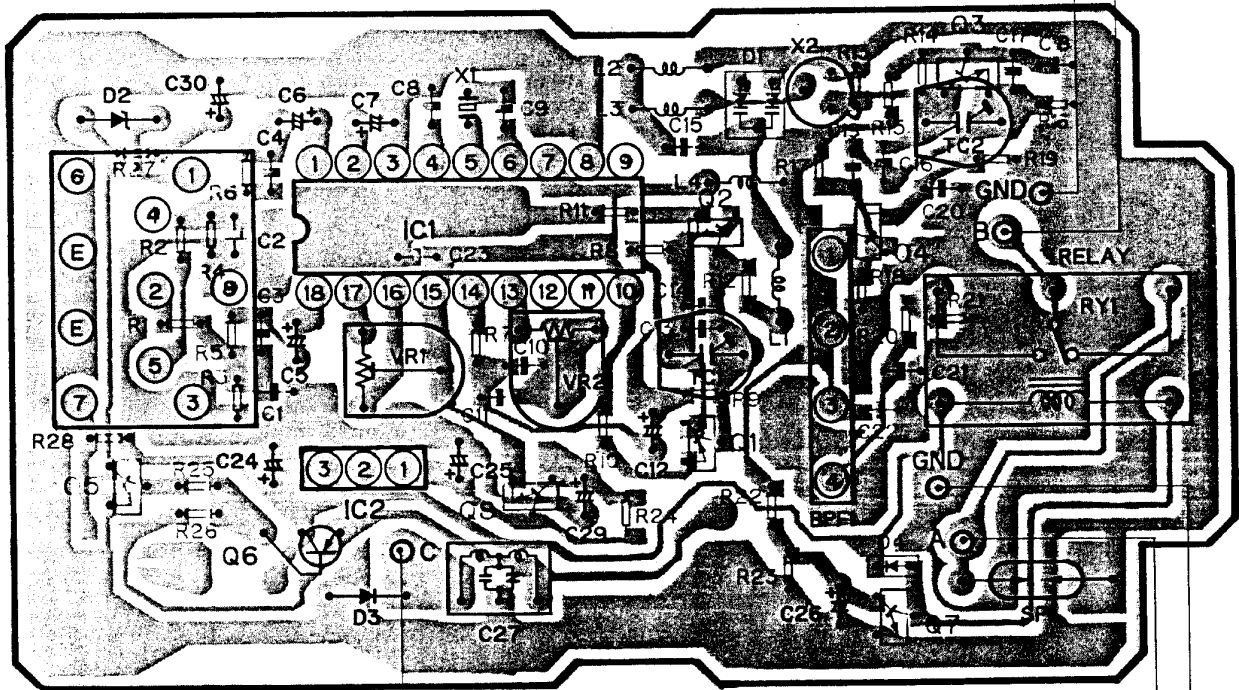
A

P.C. BOARD UNIT

IC. Q	Q5	Q6	IC2	IC1	Q8	Q1	Q2	Q4	Q7	Q3
ADJ			VR1		VR2	TC1		TC2		

B

B



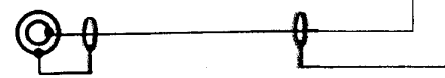
C

C

TO ANTENNA PLUG

ACC + B  
14.4 V

FUSE  
1A



D

D

Fig. 4

1

2

3

7